**Lab Assignment- 3**

**Practice Lab**

**Week 3: 18 Aug-23 Aug**

**Lab 3 (hardwired simulation tool)**

1. Design the decoder circuits with defined logic.

2. Design a Logical Unit that support Basic Logical Operation and Shift Operations.

3. Design a arithmetic Circuit that Do addition, subtraction, Multiplication and Division.

4. Design the 4 bit ALU circuits with defined operation logic having above questions logical

and arithmetic unit.